



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-60V	8Ω @ V _{GS} = -5V	-238mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- DC-DC converters
- Power-management functions

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMP68D1LVQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

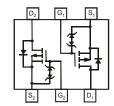
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 <a> § 3
- Weight: 0.006 grams (Approximate)







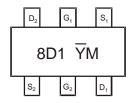
Ordering Information (Note 4)

Part Number	Package	Packing		
	Package	Qty.	Carrier	
DMP68D1LVQ-7	SOT563	3000	Tape & Reel	
DMP68D1LVQ-13	SOT563	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



8D1 = Product Type Marking Code $\overline{Y}M$ = Date Code Marking \overline{Y} = Year (ex: K = 2023) M = Month (ex: D = December)

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	Р	R	S	T	U	V	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	-60	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) $V_{GS} = -5V$ State $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		lo	-238 -190	mA	
Maximum Continuous Body Diode Forward Curren	t (Note 6)	Is	-238	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			IDM	-1	Α
Pulsed Source Current (10µs Pulse, Duty Cycle =	1%)		Ism	-1	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	258	°C/W
Total Power Dissipation (Note 6)		Pp	0.8	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	151	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-60		_	V	$V_{GS} = 0$, $I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	l		-1.0	μΑ	$V_{DS} = -60V, V_{GS} = 0$	
Gate-Source Leakage	IGSS			±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.8	_	-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	RDS(ON)	l	2.0	8	Ω	$V_{GS} = -5V, I_{D} = -100mA$	
Diode Forward Voltage	VsD	-	-0.8	-1.5	V	$V_{GS} = 0$, $I_{S} = -100 \text{mA}$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		42	_		V _{DS} = -30V, V _{GS} = 0, f = 1.0MHz	
Output Capacitance	Coss		10	_	pF		
Reverse Transfer Capacitance	Crss	l	6	_		I = 1.0IVIH2	
Gate Resistance	R_g	_	225	_	Ω	$V_{DS} = 0$, $V_{GS} = 0$, $f = 1MHz$	
Total Gate Charge	Qg	_	0.6	_		V 51/ V 001/	
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{GS} = -5V$, $V_{DS} = -30V$, $I_{D} = -100$ mA	
Gate-Drain Charge	Q _{gd}	_	0.2	_		ID = -100IIIA	
Turn-On Delay Time	t _D (ON)	_	11	_			
Turn-On Rise Time	t _R	_	16	_		$V_{GS} = -5V, V_{DS} = -30V,$	
Turn-Off Delay Time	tD(OFF)		30	_	ns	$R_G = 50\Omega$, $I_D = -100 \text{mA}$	
Turn-Off Fall Time	tF		30	_			

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



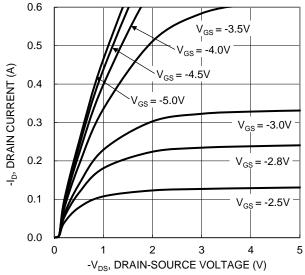


Figure 1. Typical Output Characteristic

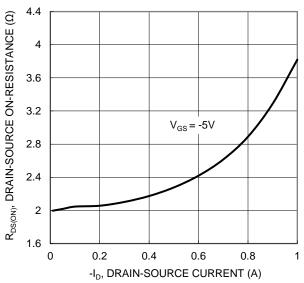


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

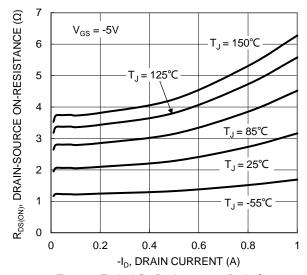


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

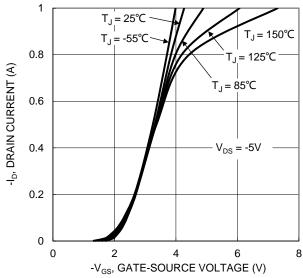


Figure 2. Typical Transfer Characteristic

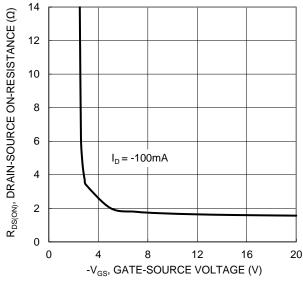


Figure 4. Typical Transfer Characteristic

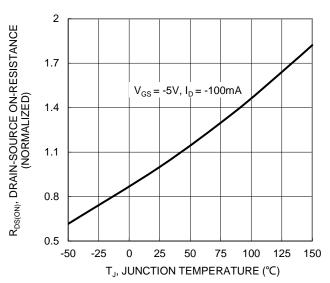


Figure 6. On-Resistance Variation with Junction Temperature



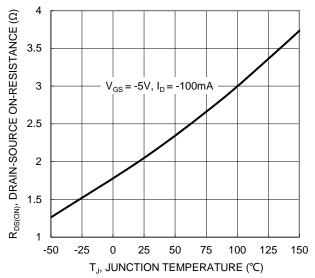


Figure 7. On-Resistance Variation with Junction Temperature

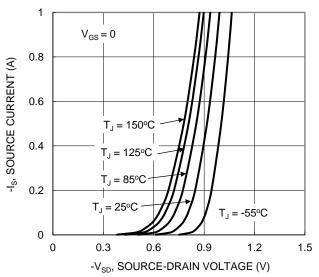


Figure 9. Diode Forward Voltage vs. Current

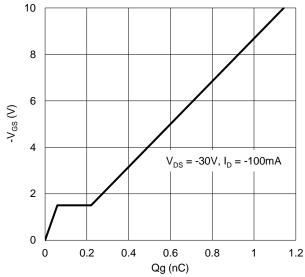


Figure 11. Gate Charge

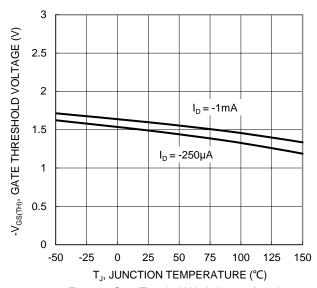


Figure 8. Gate Threshold Variation vs. Junction Temperature

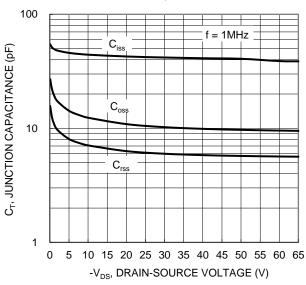
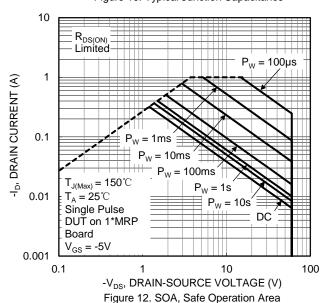


Figure 10. Typical Junction Capacitance



DMP68D1LVQ



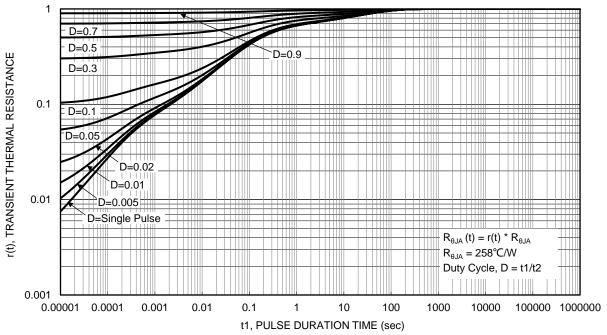


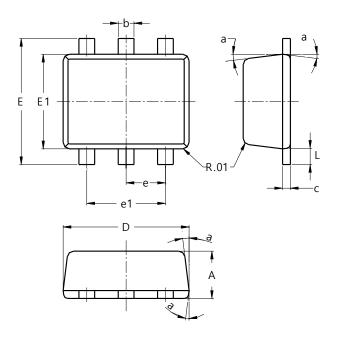
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563

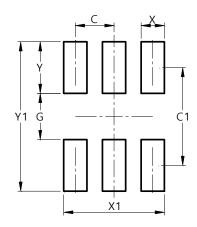


SOT563						
Dim	Min	Max	Тур			
Α	0.55	0.60				
b	0.15	0.30	0.20			
С	0.10	0.18	0.11			
D	1.50	1.70	1.60			
Е	1.55	1.70	1.60			
E1	1.10	1.25	1.20			
е			0.50			
e1	0.90	1.10	1.00			
L	0.10	0.30	0.20			
а	8°	9°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563



Dimensions	Value (in mm)
С	0.500
C1	1.270
G	0.600
Х	0.300
X1	1.300
Υ	0.670
Y1	1.940



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