



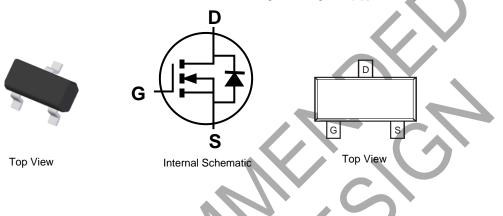
### N-CHANNEL ENHANCEMENT MODE MOSFET

### Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



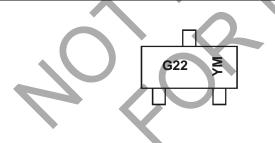
## Ordering Information (Note 4)

Part Number		Ca	se		Packaging
DMN2075U-7		SO	T23 🔺		3,000/Tape & Reel

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information

Notes:



G22 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code	кеу												
Year	2009	~	. 2	2017	2018	2019	2020	2021	202	22 2	023	2024	2025
Code	W	~	-	E	F	G	Н		J		K	L	М
Mont	:h	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	e	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit	
Drain-Source Voltage			V <sub>DSS</sub>	20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±8	V		
Continuous Drain Current (Note 5)	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	4.2 3.4	А	
Maximum Continuous Body Diode For	ote 6)	ls	1.2	A		
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	27	А	
Pulsed Body Diode Forward Current (N	lote 6)		I <sub>SM</sub>	24	A	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.8	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$	R <sub>θJA</sub>	156	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	O°

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout. 6. Repetitive rating, pulse width limited by junction temperature.

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

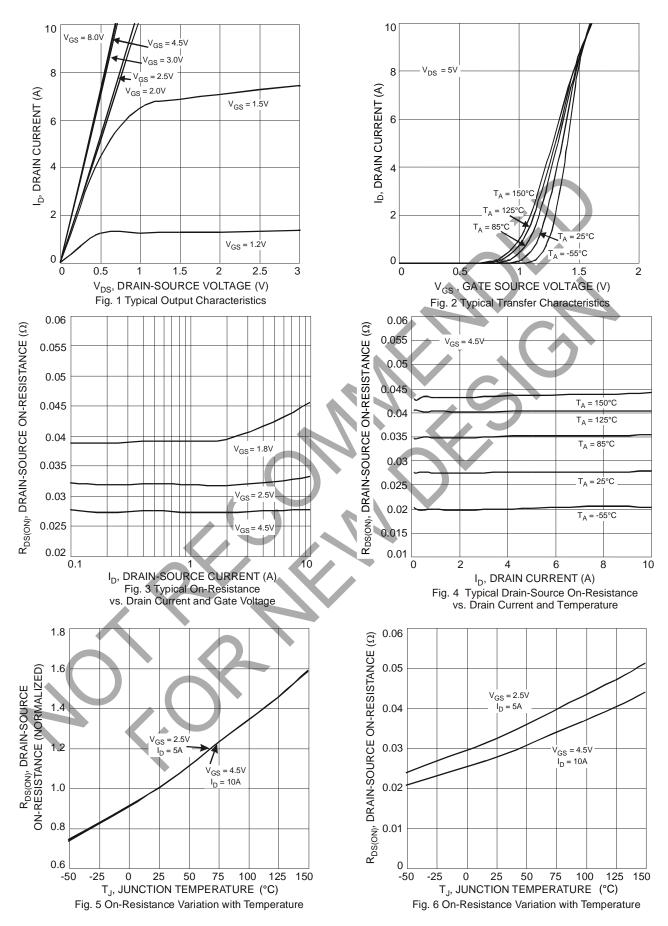
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	-		V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	IDSS		-	100	nA	$V_{DS} = 16V, V_{GS} = 0V$
Gate-Source Leakage	Igss			±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4		1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
Static Drain-Source On-Resistance	D		25 30	38 45	mΩ	$V_{GS} = 4.5V, I_D = 3.6A$
	R <sub>DS(ON)</sub>	_				$V_{GS} = 2.5V, I_D = 3.1A$
Forward Transfer Admittance	YFS	-	13		S	$V_{DS} = 5V, I_D = 3.6A$
Diode Forward Voltage	V <sub>SD</sub>	Ļ	0.75	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	CISS	•	594.3	_	pF	
Output Capacitance	Coss	—	64.5	_	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	57.7	_	pF	1 = 1.000112
Gate Resistance	Rg		1.5	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Q <sub>G</sub>	_	7.0	_	nC	
Gate-Source Charge	Q <sub>GS</sub>	—	0.9	_	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, ID = 3.6A
Gate-Drain Charge	Q <sub>GD</sub>		1.4	_	nC	ID = 3.6A
Turn-On Delay Time	t <sub>D(ON)</sub>	-	7.4	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	9.8		ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	—	28.1	—	ns	$R_L = 2.78\Omega, R_g = 1.0\Omega$
Turn-Off Fall Time	t <sub>F</sub>	_	6.7	_	ns	

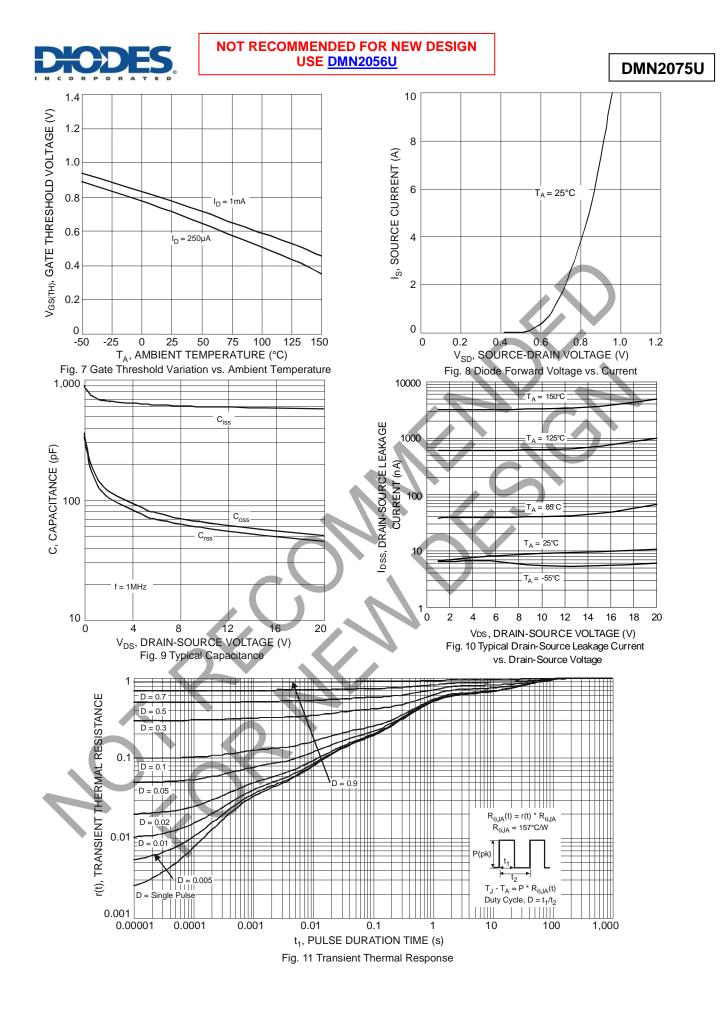
Notes:7. Short duration pulse test used to minimize self-heating effect.<br/>8. Guaranteed by design. Not subject to production testing.



## NOT RECOMMENDED FOR NEW DESIGN USE <u>DMN2056U</u>

DMN2075U

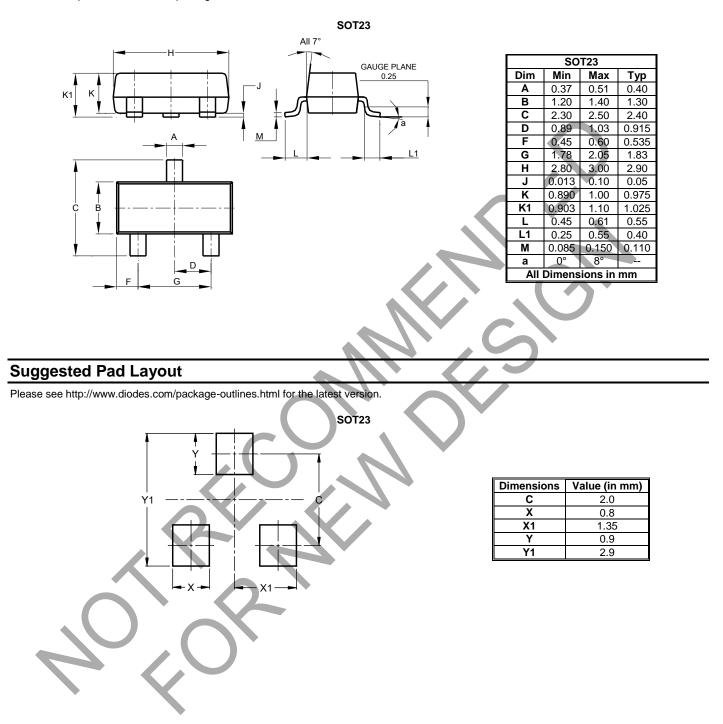






## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





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